

# **METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS**

## **Abstract of the Disclosure**

An integrated circuit, comprising: a predefined block of functional circuitry having a plurality of I/O pins; and a backside I/O pad electrically connected to each I/O pin through a backside via of the integrated circuit.

Patent Application No. 2014/0111111

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